


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

[Advanced Sc](#)
[Scholar Prefe](#)
[Scholar Help](#)

Scholar [All articles](#) - [Recent articles](#) Results **1 - 30** of about **33** for **state data set "stack priority"**. (0.19 sec)

Heterogeneous design idioms for software architecture

M Shaw - Proceedings of the 6th international workshop on Software ... , 1991 - portal.acm.org

... Program **State** (Working Memory) Blackboard ;:Knowt«dga Boule*»!; j ... alias table,"

"requisition slip **data** file." Connections ... of the elements is **set** aside, the ...

Cited by 48 - [Related articles](#) - [Web Search](#) - [All 4 versions](#)

Priority assignment apparatus for use in a memory controller

MK Webster - US Patent 4,151,598, 1979 - Google Patents

... more of those memory banks are available to receive the **data**. ... busy flip-flop associated with that level is **set**. ... means for decoding a selected **state** of the ...

Cited by 13 - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Wait-free synchronization- ►hujl.ac.il (pdf)

M Herlihy - ACM Transactions on Programming Languages and Systems (... , 1991 - portal.acm.org

... is not an event of A, and A's **state** components are ... is a **set** of processes and a **set** of objects ... threads of control, and objects represent **data** structures shared ...

Cited by 860 - [Related articles](#) - [Web Search](#) - [All 29 versions](#)

[PDF] ►THE WHETSTONE KDF9 ALGOL TRANSLATOR

B Randell - Proceedings of the Programming Systems Symposium, London ... , 1962 - jmreveillac.com

... up the normal stack positions for link **data**, etc. ... **set** of **state** variables is used

to differentiate between different ... which has at its disposal a **set** of general ...

Cited by 4 - [Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 5 versions](#)

PRIORITY RESOLUTION NETWORK FOR

US Patent 3,638,198, 1972 - Google Patents

... point matrix, and an exchange control of **data** storage units ... designate the same level from the fifth **state** from the ... second priority stage can only be **set** if the ...

Cited by 11 - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Programmable priority branch circuit

GT Davis, BD Mandalia - US Patent 4,972,342, 1990 - Google Patents

... and gate to the 120* is equal to the **state** of P3 ... out of respective regis- 1. In a **data** processing system ... next most significant bits out of respective **set** of user ...

Cited by 5 - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

Multi-level memory and methods for allocating **data** most likely to be used to the fastest memory ...

SA Thompson, CS Pawar - US Patent 5,539,893, 1996 - Google Patents

... A stack is in an Alive **state** when it is ... The TCU 33 also maintains **data** that allows it to ... stack should run on the processor next, based upon a **stack's priority**. ...

Cited by 7 - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Impossibility and universality results for wait-free synchronization- ►iitm.ernet.in (pdf)

MP Herlihy - Proceedings of the seventh annual ACM Symposium on ... , 1988 - portal.acm.org

... an event of A, and A's **state** components are ... of a composite automaton and S a **set** of component ... objects represent **data** structures shared by processes, and the ...

Cited by 141 - [Related articles](#) - [Web Search](#) - [Library Search](#) - [All 3 versions](#)

Interrupt priority control within a nested interrupt system

P Kimelman, I Field, RR Grisenthwaite - US Patent App. 10/775,334, 2004 - Google Patents

... The pending and active values can be **set** by the nested ... embodiments of the present technique utilises stack **data** storage to store **state data** corresponding to ...

[Related articles](#) - [Web Search](#) - [All 4 versions](#)

Interrupt processing control

P Kimelman, I Field - US Patent App. 10/775,335, 2004 - Google Patents

... 4 IntC (priority 1) = 3 **Stack priority** = 1 Page 5. ... 3 schematically illustrates a stack memory containing multiple **sets** of **state data**; [0035] FIG. ...

[Web Search](#) - [All 2 versions](#)

An evaluation of Penny: a system for fine grain implicit parallelism- [psu.edu \(pdf\)](#)

J Montelius, S Haridi - Proceedings of the second international symposium on ..., 1997 - portal.acm.org

... associative but on the machine we are using only one of the four **sets** is used ... All **data** structures that are used to represent the execution **state** (except AKL ...

[Cited by 5](#) - [Related articles](#) - [Web Search](#) - [All 9 versions](#)

[PDF] [Understanding unsegmented user utterances in real-time spoken dialogue systems](#)

M Nakano, N Miyazaki, JI Hirasawa, K Dohsaka, T ... - ANNUAL MEETING-ASSOCIATION FOR COMPUTATIONAL LINGUISTICS, 1999 - acl.ldc.upenn.edu

... search information and is a triplet of the following **data**. ... belief **state**: A **set** of the system's beliefs about ... produce responses based on the belief **state** in the ...

[Cited by 26](#) - [Related articles](#) - [View as HTML](#) - [Web Search](#) - [BL Direct](#) - [All 20 versions](#)

Priority token protocol circuit for a token ring local area network adaptor

A Szczepanek - US Patent 4,736,368, 1988 - Google Patents

... nal successful copy and/or address recognition by **set**- tje ... sequence of states and allowed in the **data** path for ... and outputs of the priority token **state** plished ...

[Cited by 11](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Multiple image digital processing system

PRN Kellar - US Patent 4,360,831, 1982 - Google Patents

... at any one time and outputs this picture **data**. ... arrangement if a change in **stack priority** is chosen ... will typically employ known solid **state** switching techniques ...

[Cited by 34](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Branch on bit processing.

GT Davis, BD Mandalia, P It - 1990 - freepatentsonline.com

... loading instructions of the processor, such as **Data** Move instructions ... to this OR gate are OFF P3 is **set** OFF ... respective register 120x is equal to the **state** of P3 ...

[Web Search](#) - [All 2 versions](#)

System and method for window priority rendering

LR Blume - US Patent App. 10/251,875, 2002 - Google Patents

... speakers or any device for providing **data** in audio ... A window stack is denned as the **set** of windows ... For each window in the window **stack**: **Priority** = window level ...

[Web Search](#) - [All 2 versions](#)

Method for generating interrupt commands in a microprocessor system and relative priority interrupt

...
S Pezzini - US Patent App. 10/717,177, 2003 - Google Patents

... 19, 2003 (30) Foreign Application Priority **Data** Nov. ... A **state** machine IRQ SM forms the core of the ... its priority is canceled from the **stack PRIORITY STACK** by a ...

[Web Search](#) - [All 5 versions](#)

Method and device to calculate activity degrees of method programs

T Ogasawara - US Patent 6,671,877, 2003 - Google Patents

... To discard a code in such a **state**, all threads are ... Then, relational **data** of BB's starting address i addr and ... by m1 are provided in effective call **set SET**^{nij} ...

Cited by 1 - Related articles - Web Search - All 2 versions

Enhanced thread processing

LM Browning - US Patent 6,654,781, 2003 - Google Patents

... of control such as **stack**, **priority**, a **set** of pending ... operating system to hold the register **state** of the ... Thread private **data** is almost always only referenced by ...

Cited by 1 - Related articles - Web Search - All 2 versions

Prototyping QoS based architecture for power plant control applications- ► cnam.fr (PDF)C Lizzi, L Bacon, E Becquet, E Gressier-Soudan - 2000 IEEE International Workshop on Factory Communication ..., 2000 - ieeexplore.ieee.org... as a tuple whose terms constitute a **set** of scheduling ... and one interface to control the binding **state** (monitoring ... Our Java threads are able to exchange **data** at a ...

Cited by 7 - Related articles - Web Search - All 3 versions

Experimental evaluation of real-time support on the Mercuri wide area ATM testbedA Pavan, J Liu, A Guha, J Pugaczewski, M Midami - CONFERENCE ON LOCAL COMPUTER NETWORKS, 1995 - doi.ieeecomputersociety.org... Figure 8: The **set** up for Experiments 2 and 3 ... Also, the load traffic was a constant **data** rate stream ... The steady **state** throughput of the tcp program was observed ...

Cited by 5 - Related articles - Web Search - BL Direct - All 4 versions

Design and performance of a DSI terminal for domestic applicationsR Maruta, A Tomozawa, T Nishitani, T Okada, K ... - Communications, IEEE Transactions on [legacy, pre-1988], 1981 - ieeexplore.ieee.org... digital delay, used to off- **set** speech detection ... kovian model whose **state** transition is controlled by random ... a feedback shift register, and **data** channels were ...

Cited by 3 - Related articles - Web Search

[PDF] ► Quantum Programming Languages—A Survey—R Rudiger - 2003 - public.fh-wolfenbuettel.de... is seen as a sequence of **state** transitions on a **set** ... fied, then it will result in a **state** in which ... **data** and operations acting on these **data** are combined to ...

Cited by 1 - Related articles - View as HTML - Web Search - All 5 versions

Reducing null messages in Misra's distributed discrete eventsimulation methodRC De Vries - IEEE Transactions on Software Engineering, 1990 - ieeexplore.ieee.org... Jayaraman [5] applies **data** flow concepts to the DDES problem and Kumar [6] specifically ... the local time will be that of the lowest time over the **set** of channel ...

Cited by 35 - Related articles - Web Search - All 8 versions

An architecture for voice dialog systems based on prolog-style theorem proving- ► upenn.edu (PDF)RW Smith, AW Biermann, DR Hipp - Computational Linguistics, 1995 - portal.acm.org... The paper gives performance **data** for a series of 141 problem ... The **set** of logical rules leading to ... an action to achieve or observe a physical **state** was completed ...

Cited by 42 - Related articles - Web Search - BL Direct - All 13 versions

[PS] ► A comparison of different statistical machine translation approaches for Spanish-to-Basque ...J Gonzalez, D Ortiz, J Tomas, F Casacuberta - Terceras Jornadas en Tecnologia del Habla, Valencia, 2004 - prhit.it.es... as in [18] that uses a **set** of stacks ... de configuracion)) 2. Infer a finite-**state** automaton: a ... of Spanish-to-Basque translation through a **dataset** of increasing ...

[Cited by 2](#) - [Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 3 versions](#)

A realization of a concurrent object-oriented programming

CH Jo, CH Lee, JG Son - Proceedings of the 1998 ACM symposium on Applied Computing, 1998 - portal.acm.org
... to allocate stacks, heap area, method table, and **data** area for ... own **state** such as
thread ID, register **state**(PC, stack pointer), **stack**, **priority**, and thread ...

[Cited by 3](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

Supervised single link telephone system

JE Dahlquist, DT Singhi - US Patent 4,081,614, 1978 - Google Patents
... 11/1976 6/1977 An administrative telephone system for use in schools, hospitals
and the like which includes a dialable adminis- trative telephone **set** and a ...

[Cited by 4](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

A multi-exchange neighborhood for minimum makespan parallel machine scheduling problems-

► [unipl.it](#) (PDF)

A Frangioni, E Necciari, MG Scutellà - Journal of Combinatorial Optimization, 2004 - Springer
... In order to **state** such a characterization, a further definition is required ... $j \in r$,
 $j \in 1$) such that the **set** of the machines involved into the exchange, ie, $\{M(j) \mid j \in r, j \in 1\}$...

[Cited by 35](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 7 versions](#)

Method and apparatus for correcting an internal call/return stack in a microprocessor that detects ...

TC McDonald - US Patent App. 10/643,338, 2003 - Google Patents
... includes a user-visible register **set**, in addition to ... the microinstructions based
on the **data** and oper ... 108 registers, thereby updating microprocessor 100 **state**. ...

[Related articles](#) - [Web Search](#) - [All 4 versions](#)

Key authors: [M Herlihy](#) - [M Shaw](#) - [P Kellar](#) - [M Nakano](#) - [M Webster](#)

Google ►

Result Page: 1 2 [Next](#)

state data set "stack priority "

[Search](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2009 Google